





60V DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	I _D T _A = 25°C		
60V	66mΩ @ V _{GS} = 10V	4.4A		
60 V	97mΩ @ V _{GS} = 4.5V	3.6A		

Description and Applications

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor control
- Backlighting
- DC-DC Converters
- Power management functions

Features and Benefits

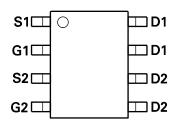
- Low on-resistance
- Fast switching speed
- "Green" component and RoHS compliant (Note 1)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

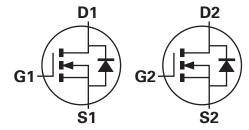
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0 (Note 1)
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See diagram below
- Terminals: Finish Matte Tin annealed over Copper lead frame.
 Solderable per MIL-STD-202, Method 208
- Weight: 0.074 grams (approximate)







Top View



Equivalent Circuit

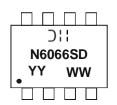
Ordering Information (Note 1)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMN6066SSD-13	N6066SD	13	12	2,500

1. Diodes, Inc. defines "Green" products as those which are RoHS compliant and contain no halogens or antimony compounds; further information about Diodes Inc.'s "Green" Policy can be found on our website. For packaging details, go to our website.

Marking Information

Note:



DII = Manufacturer's Marking
N6066SD = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 09 = 2009)
WW = Week (01-53)





Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source voltage			V_{DSS}	60	V
Gate-Source voltage (Note 2)			V_{GS}	±20	V
		(Note 4)		4.4	
Continuous Drain current	$V_{GS} = 10V$	$T_A = 70^{\circ}C$ (Note 4)	I_{D}	3.5	Α
		(Note 3)		3.3	
Pulsed Drain current V _{GS} = 10V		(Note 5)	I _{DM}	17.0	Α
Continuous Source current (Body diode)		(Note 4)	I _S	3.2	Α
Pulsed Source current (Body diode)		(Note 5)	I _{SM}	17.0	Α

Thermal Characteristics @TA = 25°C unless otherwise specified

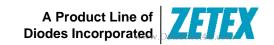
Characteristic	Symbol	Value	Unit		
	(Notes 3 & 6)		1.25 10		
Power dissipation Linear derating factor	(Notes 3 & 7)	P _D	1.8 14.3	W mW/°C	
	(Notes 4 & 6)		2.14 17.2		
	(Notes 3 & 6)		100		
Thermal Resistance, Junction to Ambient	(Notes 3 & 7)	$R_{ heta JA}$	70	20.44	
	(Notes 4 & 6)		58	°C/W	
Thermal Resistance, Junction to Lead	(Notes 6 & 8)	$R_{ heta JL}$	55		
Operating and storage temperature range		T_{J}, T_{STG}	-55 to 150	°C	

Notes:

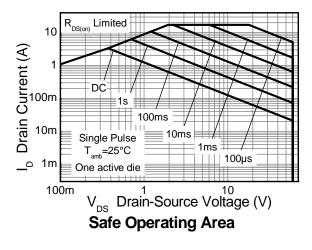
- 2. AEC-Q101 V_{GS} maximum is $\pm 16V$.
- 3. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 4. Same as note (3), except the device is measured at t ≤ 10 sec.
 5. Same as note (3), except the device is pulsed with D= 0.02 and pulse width 300 μs. The pulse current is limited by the maximum junction temperature.
 6. For a device with one active die.
 7. For a device with two active die running at equal power.

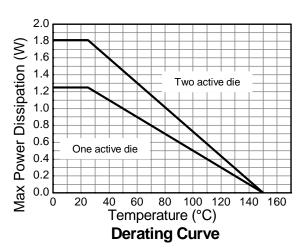
- 8. Thermal resistance from junction to solder-point (at the end of the drain lead).

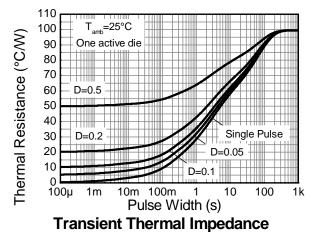


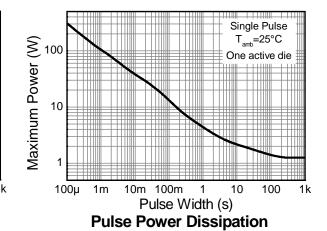


Thermal Characteristics













Electrical Characteristics @TA = 25°C unless otherwise specified

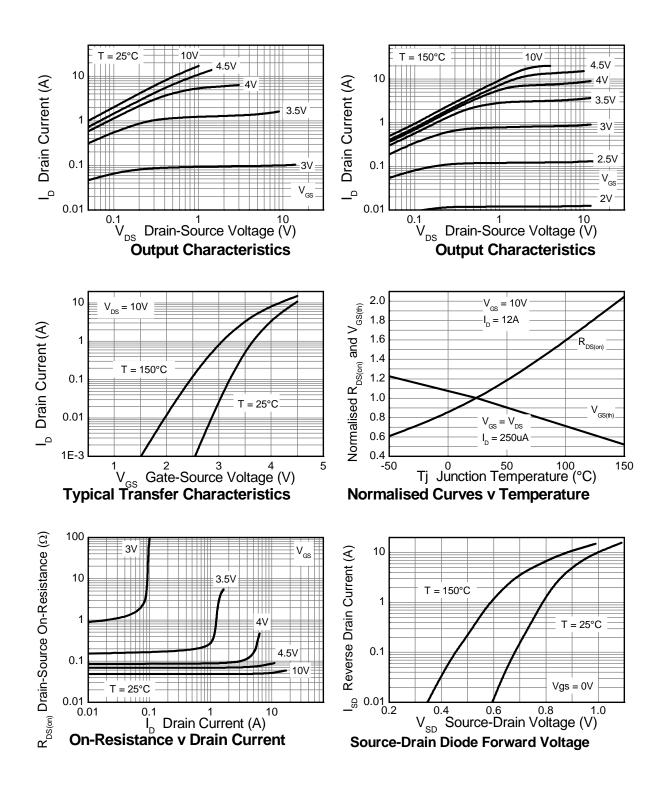
Characteristic	Symbol	Min	Тур	Max	Unit	Test C	ondition	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV_{DSS}	60	_	_	V	I _D = 250μA, V _{GS} = 0V		
Zero Gate Voltage Drain Current	I _{DSS}	_	_	0.5	μΑ	V_{DS} = 60V, V_{GS} =	0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS}$	= 0V	
ON CHARACTERISTICS								
Gate Threshold Voltage	V _{GS(th)}	1.0	_	3.0	V	I _D = 250μA, V _{DS} =	: V _{GS}	
Static Drain Source On Registence (Note 0)	В		0.048	0.066	Ω	V _{GS} = 10V, I _D = 4.5A		
Static Drain-Source On-Resistance (Note 9)	R _{DS (ON)}	_	0.068	0.097	12	$V_{GS} = 4.5V, I_{D} = 3$	3.5A	
Forward Transconductance (Notes 9 & 10)	9fs	_	19.2	_	S	V _{DS} = 15V, I _D = 6/	4	
Diode Forward Voltage (Note 9)	V_{SD}	_	0.89	1.15	V	I _S = 4.5A, V _{GS} = 0V		
Reverse recovery time (Note 10)	t _{rr}		22.2	_	ns	I _S = 1.9A, di/dt= 100A/μs		
Reverse recovery charge (Note 10)	Q_{rr}	_	16.9	_	nC	1S= 1.9A, di/di= 1	ιουΑ/μς	
DYNAMIC CHARACTERISTICS (Note 10)								
Input Capacitance	C _{iss}	_	502	_	pF	.,	0.1	
Output Capacitance	Coss	_	45.7	_	рF	V _{DS} = 30V, V _{GS} = -f= 1MHz	UV	
Reverse Transfer Capacitance	Crss	_	27.1	_	pF]=		
Total Gate Charge (Note 11)	Q_g	_	5.4	_	nC	V _{GS} = 4.5V		
Total Gate Charge (Note 11)	Qq	_	10.3	_	nC		V _{DS} = 30V	
Gate-Source Charge (Note 11)	Q_{gs}	_	1.7	_	nC	V _{GS} = 10V	$I_{D} = 4.5A$	
Gate-Drain Charge (Note 11)	Q_{gd}	_	3.2	_	nC	1		
Turn-On Delay Time (Note 11)	t _{D(on)}	_	2.7	_	ns			
Turn-On Rise Time (Note 11)	t _r	_	2.4	_	ns	V _{DD} = 30V, V _{GS} = 10V		
Turn-Off Delay Time (Note 11)	t _{D(off)}	_	14.7	_	ns	I _D = 1A, R _G ≅ 6.09	Ω	
Turn-Off Fall Time (Note 11)	t _f	_	5.4	_	ns	1 '-		

Notes:

- 9. Measured under pulsed conditions. Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$
- For design aid only, not subject to production testing.
 Switching characteristics are independent of operating junction temperatures.

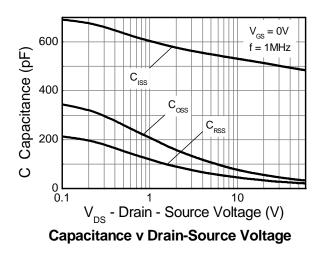


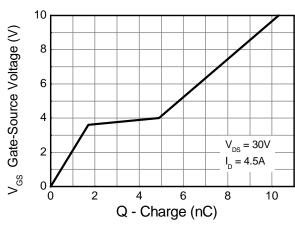
Typical Characteristics





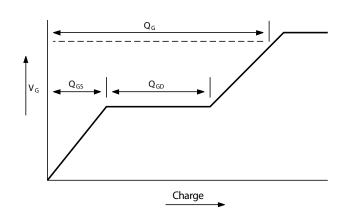
Typical Characteristics - continued





Gate-Source Voltage v Gate Charge

Test Circuits



Current regulator

12V 0.2 \(\mu \) F 50k D.U.T

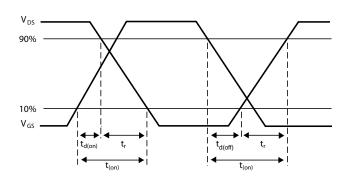
V_{DS}

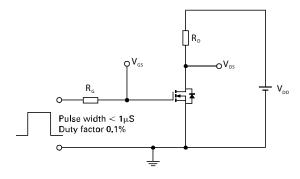
V_{SS}

D.U.T

Basic gate charge waveform

Gate charge test circuit



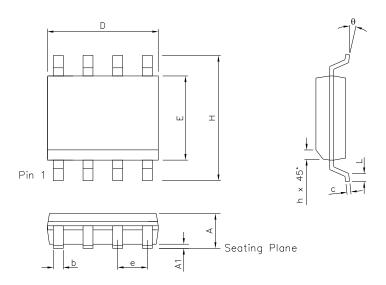


Switching time waveforms

Switching time test circuit

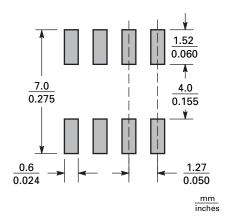


Package Outline Dimensions



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
Α	0.053	0.069	1.35	1.75	е	0.050 BSC		1.27 BSC	
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51
D	0.189	0.197	4.80	5.00	С	0.008	0.010	0.19	0.25
Н	0.228	0.244	5.80	6.20	θ	0°	8°	0°	8°
Е	0.150	0.157	3.80	4.00	h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27	-	-	-		-

Suggested Pad Layout







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